

## Refine Search

### Search Results -

Term	Documents
MICROCONTROLLER\$1	0
MICROCONTROLLER	38573
MICROCONTROLLERI	1
MICROCONTROLLERS	7007
MICROCONTROLLERT	1
(16 AND MICROCONTROLLER\$1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	19
(L16 AND MICROCONTROLLER\$1 ).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	19

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L17

Refine Search

Recall Text

Clear

Interrupt

### Search History

DATE: Thursday, June 03, 2004   [Printable Copy](#)   [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<u>L17</u>	L16 and microcontroller\$1	19	<u>L17</u>
<u>L16</u>	L15 and ROM and EEPROM	207	<u>L16</u>
<u>L15</u>	L14 and (((integrated adj circuit adj card\$1) or (IC adj card\$1) or (smart adj card\$1))) .clm.	922	<u>L15</u>
<u>L14</u>	l5 and (((integrated adj circuit adj card\$1) or (IC adj card\$1) or (smart adj card\$1))) .ti.	5311	<u>L14</u>
<u>L13</u>	l11 and ROM and EEPROM	1	<u>L13</u>
<u>L12</u>	L11 and microcontroller\$1	0	<u>L12</u>

<u>L11</u>	L6 and (custom\$ near3 circuit\$1)	27	<u>L11</u>
<u>L10</u>	L7 and (custom\$ near3 circuit\$1)	0	<u>L10</u>
<u>L9</u>	L8 and (custom\$ near3 circuit\$1)	0	<u>L9</u>
<u>L8</u>	L7 and ROM and EEPROM	7	<u>L8</u>
<u>L7</u>	L6 and (microcontroller\$1 same (input/output or i/o or input or output))	24	<u>L7</u>
<u>L6</u>	L5 and (((integrated adj circuit adj card\$1) or (IC adj card\$1) or (smart adj card\$1)) and (terminal\$1 or (interface adj device\$1))).ab.	6715	<u>L6</u>
<u>L5</u>	((integrated adj circuit adj card\$1) or (IC adj card\$1) or (smart adj card\$1)) and (terminal\$1 or (interface adj device\$1))	17992	<u>L5</u>
<u>L4</u>	L3 and (custom\$ near3 circuit\$1)	0	<u>L4</u>
<u>L3</u>	L2 and (microcontroller\$1 same (input/output or i/o))	20	<u>L3</u>
<u>L2</u>	L1 and ROM and EEPROM	995	<u>L2</u>
<u>L1</u>	((integrated adj circuit adj card\$1) or (IC adj card\$1)) and (terminal\$1 or (interface adj device\$1))	11422	<u>L1</u>

END OF SEARCH HISTORY